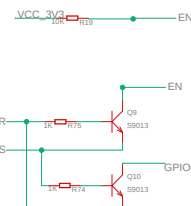


Pin connection diagram for the CH340C USB-to-UART bridge chip. The chip is shown with its 16 pins. Pin 1 (GND) is connected to the common ground. Pin 2 (TXD) is connected to UART0_RXD0. Pin 3 (RXD) is connected to UART0_TXD0. Pin 4 (V3) is connected to VCC_3V3. Pin 5 (UD+) is connected to USB_DP. Pin 6 (UD-) is connected to USB_DN. Pin 7 (X1) is connected to ground. Pin 8 (XTS#) is connected to ground. Pin 9 (VCC) is connected to VCC_3V3. Pin 10 (DSR#) is connected to ground. Pin 11 (RTS#) is connected to RTS. Pin 12 (DTR#) is connected to DTR. Pin 13 (RS232) is connected to ground. Pin 14 (RTS) is connected to RTS. Pin 15 (DTR) is connected to DTR. Pin 16 (VCC_3V3) is connected to VCC_3V3. A 100nF capacitor is connected between pins 9 and 16.



The figure consists of two circuit diagrams showing pin connections for different modules.

Left Diagram (NS2009):

- NS2009 Pinout:**
 - 1: SCL VDD
 - 2: SDA XP
 - 3: A0 YP
 - 4: PENIRQN
 - 5: GND YN
 - 6: GND
 - 7: PENI
 - 8: A1 YP
 - 9: SDA XD
 - 10: GPIO39-SCL
 - 11: GPIO39-SDA
- Connections:**
 - VCC_3V3 is connected to pin 1 (SCL VDD) and pin 10 (GPIO39-SCL).
 - GND is connected to pins 5 (GND YN) and 6 (GND).
 - PENI is connected to pin 7 (PENI).
 - GPIO39-SCL is connected to pin 10 (GPIO39-SCL).
 - GPIO39-SDA is connected to pin 11 (GPIO39-SDA).

Right Diagram (FT6236):

- FT6236 Pinout:**
 - 1: GPIO39-SCL
 - 2: GPIO39-SDA
 - 3: EN
 - 4: P2
 - 5: P2
 - 6: P2
 - 7: P2
 - 8: P2
 - 9: P2
 - 10: P2
 - 11: P2
 - 12: P2
 - 13: P2
 - 14: P2
 - 15: P2
 - 16: P2
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 - 90: P2
 - 91: P2
 - 92: P2
 - 93: P2
 - 94: P2
 - 95: P2
 - 96: P2
 - 97: P2
 - 98: P2
 - 99: P2
 - 100: P2
- Connections:**
 - VCC_3V3 is connected to pin 1 (GPIO39-SCL) and pin 2 (GPIO39-SDA).
 - GND is connected to pins 5 (EN) and 6 (P2).
 - PENI is connected to pin 3 (EN).
 - GPIO39-SCL is connected to pin 1 (GPIO39-SCL).
 - GPIO39-SDA is connected to pin 2 (GPIO39-SDA).

The schematic shows the electrical connections between the LCD module and the microcontroller. The LCD module is represented by a large rectangle containing a pin header with pins numbered 1 through 40. The pins are labeled as follows:

- Pins 1-6: X(L), XU, XR, Y(L), YR, VDD
- Pins 7-8: VCC_3V3
- Pins 9-10: VCC_3V3
- Pins 11-12: GND
- Pins 13-14: GPIO46, EN
- Pins 15-16: SPI_SDA, SPI_SCL
- Pins 17-18: SPI_MISO, SPI_MOSI
- Pins 19-20: CSBP_CS, CSBP_RS
- Pins 21-22: RSPF_AO, RSPF_DO
- Pins 23-24: SDOA_SDA, SDOA_SCL
- Pins 25-26: SDOA_MISO, SDOA_MOSI
- Pins 27-28: DB0, DB1
- Pins 29-30: DB2, DB3
- Pins 31-32: DB4, DB5
- Pins 33-34: DB6, DB7
- Pins 35-36: DB8, DB9
- Pins 37-38: DB10, DB11
- Pins 39-40: DB12, DB13

The microcontroller side of the circuit includes:

- VCC_3V3**: A 3.3V power supply connected to the VCC_3V3 pins of the LCD.
- GND**: Ground connections at various points along the signal lines.
- GPIO46**: Connected to pin 14 (EN) via a 10kΩ pull-down resistor (R10).
- Q1**: An NMOS transistor (NMOS7002) used as a switch or buffer.
- IM0, IM1, IM2**: Input multiplexing signals connected to pins 39, 40, and 1 respectively.
- R11, R5, R7**: Resistors connected to the input multiplexing signals.
- R13**: A resistor connected to the IM2 signal line.

5V电源电路

[illegible]